

CS412/413

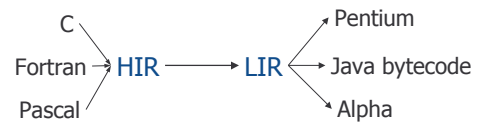
Introduction to Compilers Radu Rugina

Lecture 17: IR Lowering
03 Mar 04

Intermediate Code

- Usually two IRs:

High-level IR Language-independent (but closer to language)	Low-level IR Machine independent (but closer to machine)
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High-level IR

- **Tree node structure** very similar to the **AST**
- Contains high-level constructs common to many languages
 - Expression nodes
 - Statement nodes
- Expression nodes for:
 - Integers and program variables
 - Binary operations: $e1 \text{ OP } e2$
 - Arithmetic operations
 - Logic operations
 - Comparisons
 - Unary operations: $\text{OP } e$
 - Array accesses: $e1[e2]$

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High-level IR

- Statement nodes:
 - Block statements (statement sequences): $(s1, \dots, sN)$
 - Variable assignments: $v = e$
 - Array assignments: $e1[e2] = e3$
 - If-then-else statements: $\text{if } c \text{ then } s1 \text{ else } s2$
 - If-then statements: $\text{if } c \text{ then } s$
 - While loops: $\text{while } (c) \ s$
 - Function call statements: $f(e1, \dots, eN)$
 - Return statements: return or $\text{return } e$
- May also contain:
 - For loop statements: $\text{for}(v = e1 \text{ to } e2) \ s$
 - Break and continue statements
 - Switch statements: $\text{switch}(e) \{ v1: s1, \dots, vN: sN \}$

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Low-Level IR

- Low-level representation is essentially an instruction set for an **abstract machine**
- Alternatives for low-level IR:
 - **Three-address code** or **quadruples** (Dragon Book):
 $a = b \text{ OP } c$
 - **Tree representation** (Tiger Book)
 - **Stack machine** (like Java bytecode)

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Three-Address Code

- In this class: **three-address code**
 $a = b \text{ OP } c$
- Has at most three addresses (may have fewer)
- Also named **quadruples** because can be represented as: (a, b, c, OP)
- Example:
 $a = (b+c)*(-e);$ $t1 = b + c$
 $t2 = -e$
 $a = t1 * t2$

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Low IR Instructions

- Assignment instructions:
 - Binary operations: $a = b \text{ OP } c$
 - arithmetic: ADD, SUB, MUL, DIV, MOD
 - logic: AND, OR, XOR
 - comparisons: EQ, NEQ, LT, GT, LEQ, GEQ
 - Unary operation $a = \text{OP } b$
 - Arithmetic MINUS or logic NEG
 - Copy instruction: $a = b$
 - Load /store: $a = *b, *a = b$
 - Other data movement instructions

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Low IR Instructions (Ctd)

- Flow of control instructions:
 - label L : label instruction
 - jump L : Unconditional jump
 - cjump a L : conditional jump
- Function call
 - call $f(a_1, \dots, a_n)$
 - $a = \text{call } f(a_1, \dots, a_n)$
 - Is an extension to quads
- ... IR describes the Instruction Set of an abstract machine

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Example

```
m = 0;
if (c == 0) {
    m = m + n * n;
} else {
    m = m + n;
}
```



```
m = 0
t1 = c == 0
tjump t1 trueb
m = m+n
jump end
label trueb
t2 = n * n
m = m + t2
label end
```

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How To Translate?

- May have nested language constructs
 - Nested if and while statements
- Need an algorithmic way to translate
- Solution:
 - Start from the AST representation
 - Define translation for each node in the AST
 - Recursively translate nodes in the AST

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Notation

- Use the following notation:
 - $T[e]$ = the low-level IR representation of high-level IR construct e
 - $T[e]$ is a sequence of Low-level IR instructions
 - If e is an expression (or a statement expression), it represents a value
 - Denote by $t = T[e]$ the low-level IR representation of e, whose result value is stored in t
 - For variable v: $t = T[v]$ is the copy instruction $t = v$

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Translating Expressions

- Binary operations: $t = T[e1 \text{ OP } e2]$
(arithmetic operations and comparisons)

```
t1 = T[ e1 ]
t2 = T[ e2 ]
t = t1 OP t2
```



- Unary operations: $t = T[\text{OP } e]$

```
t1 = T[ e ]
t = OP t1
```



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Translating Boolean Expressions

- $t = T[e1 \text{ OR } e2]$

```
t1 = T[ e1 ]
t2 = T[ e2 ]
t = t1 OR t2
```



- ... how about short-circuit OR?
- Should compute e2 only if e1 evaluates to false

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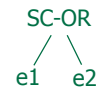
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Translating Short-Circuit OR

- Short-circuit OR: $t = T[e1 \text{ SC-OR } e2]$

```
t = T[ e1 ]
tjump t Lend
t = T[ e2 ]
label Lend
```



- ... how about short-circuit AND?

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Translating Short-Circuit AND

- Short-circuit AND: $t = T[e1 \text{ SC-AND } e2]$

```
t = T[ e1 ]
tjump t Lnext
jump Lend
label Lnext
t = T[ e2 ]
label Lend
```



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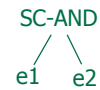
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Another Translation

- Short-circuit AND: $t = T[e1 \text{ SC-AND } e2]$

```
t = T[ e1 ]
fjump t Lend
t = T[ e2 ]
label Lend
```



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Array and Field Accesses

- Array access: $t = T[v[e]]$

```
t1 = T[ e ]
t = v[t1]
```



- Field access: $t = T[e1.f]$

```
t1 = T[ e1 ]
t = t1.f
```



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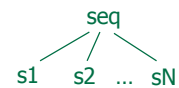
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Translating Statements

- Statement sequence: $T[s1; s2; \dots; sN]$

```
T[ s1 ]
T[ s2 ]
...
T[ sN ]
```



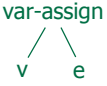
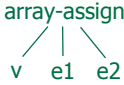
- IR instructions of a statement sequence = concatenation of IR instructions of statements

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Assignment Statements

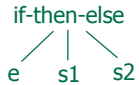
- Variable assignment: $T[v = e]$
 $v = T[e]$

- Array assignment: $T[v[e1] = e2]$
 $t1 = T[e1]$
 $t2 = T[e2]$
 $v[t1] = t2$


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Translating If-Then-Else


- $T[\text{if } (e) \text{ then } s1 \text{ else } s2]$
 $t1 = T[e]$
 $\text{fjump } t1 \text{ Lfalse}$
 $T[s1]$
 $\text{jump } \text{Lend}$
 $\text{label } \text{Lfalse}$
 $T[s2]$
 $\text{label } \text{Lend}$


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Translating If-Then


- $T[\text{if } (e) \text{ then } s]$
 $t1 = T[e]$
 $\text{fjump } t1 \text{ Lend}$
 $T[s]$
 $\text{label } \text{Lend}$


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While Statements

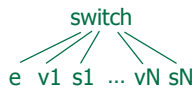
- $T[\text{while } (e) \{ s \}]$
 $\text{label } \text{Ltest}$
 $t1 = T[e]$
 $\text{fjump } t1 \text{ Lend}$
 $T[s]$
 $\text{jump } \text{Ltest}$
 $\text{label } \text{Lend}$


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Switch Statements

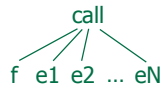

- $T[\text{switch } (e) \{ \text{case } v1: s1, \dots, \text{case } vN: sN \}]$
 $t = T[e]$
 $c = t != v1$
 $\text{tjump } c \text{ L2}$
 $T[s1]$
 $\text{jump } \text{Lend}$
 $\text{label } \text{L2}$
 $c = t != v2$
 $\text{tjump } c \text{ L3}$
 $T[s2]$
 $\text{jump } \text{Lend}$
 \dots
 $\text{label } \text{LN}$
 $c = t != vN$
 $\text{tjump } c \text{ Lend}$
 $T[sN]$
 $\text{label } \text{Lend}$


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Call and Return Statements

- $T[\text{call } f(e1, e2, \dots, eN)]$
 $t1 = T[e1]$
 $t2 = T[e2]$
 \dots
 $tN = T[eN]$
 $\text{call } f(t1, t2, \dots, tN)$

- $T[\text{return } e]$
 $t = T[e]$
 $\text{return } t$


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Statement Expressions

- So far: statements which do not return values
- Easy extensions for statement expressions:
 - Block statements
 - If-then-else
 - Assignment statements
- $t = T[s]$ is the sequence of low IR code for statement s , whose result is stored in t

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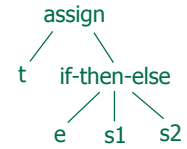
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Statement Expressions

- $t = T[\text{if}(e) \text{ then } s1 \text{ else } s2]$

$t1 = T[e]$
 $\text{cjump } t1 \text{ Ltrue}$
 $t = T[s2]$
 jump Lend
 label Ltrue
 $t = T[s1]$
 label Lend



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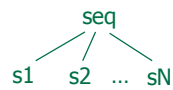
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Block Statements

- $t = [s1; s2; \dots; sN]$

$[s1]$
 $[s2]$
 \dots
 $t = [sN]$



- Result value of a block statement = value of last statement in the sequence

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Assignment Statements

- $t = [v = e]$

$v = [e]$
 $t = v$



- Result value of an assignment statement = value of the assigned expression

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