



CS 412 Introduction to Compilers

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Lecture 14: Syntax-directed translation
23 Feb 01

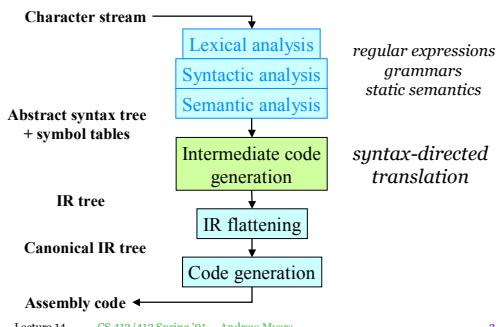
Administration

- Read: Appel 6-8
- Prelim March 6, 7:30-9:30PM

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Where we are



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IR expressions

- **CONST(*i*)** : the integer constant *i*
- **TEMP(*t*)** : a temporary register *t*. The abstract machine has an infinite number of these
- **OP(*e₁*, *e₂*)** : one of the following operations
 - arithmetic: ADD, SUB, MUL, DIV, MOD
 - bit logic: AND, OR, XOR, LSHIFT, RSHIFT, ARSHIFT
 - comparisons: EQ, NEQ, LT, GT, LEQ, GEQ
- **MEM(*e*)** : contents of memory locn w/ address *e*
- **CALL(*e₁*, *e₂*, ..., *e_n*)** : result of fn *e₁* applied to arguments *e₂*, ..., *e_n*
- **NAME(*n*)** : address of the statement or global data location labeled *n* (TBD)
- **ESEQ(*s*, *e*)** : result of *e* after stmt *s* is executed

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IR statements

- **MOVE(*dest*, *e*)** : move result of *e* into *dest*
 - *dest* = TEMP(*t*) : assign to temporary *t*
 - *dest* = MEM(*e*) : assign to memory locn *e*
- **EXP(*e*)** : evaluate *e*, discard result
- **SEQ(*s₁*, ..., *s_n*)** : execute each stmt *s_i* in order
- **JUMP(*e*)** : jump to address *e*
- **CJUMP(*e*, *l₁*, *l₂*)** : jump to statement named *l₁* or *l₂* depending on whether *e* is true or false
- **LABEL(*n*)** : a labeled statement (may be used in NAME, CJUMP)

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Translation

- Intermediate code generation is tree translation

Abstract syntax tree \Rightarrow IR tree

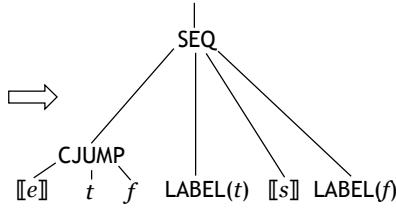
- Each subtree of AST translated to subtree in IR tree
- Translated version of AST subtree *e* is IR subtree $\llbracket e \rrbracket$

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Translating if

$\text{if } (e) s$



$\llbracket \text{if } (e) s \rrbracket = \text{SEQ}(\text{CJUMP}(\llbracket e \rrbracket, t, f), \text{LABEL}(t), \llbracket s \rrbracket, \text{LABEL}(f))$

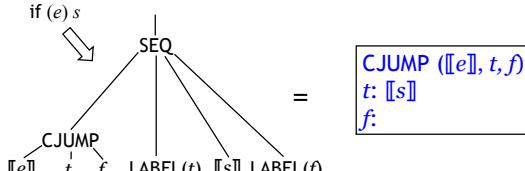
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How to read IR trees

- Think of SEQ nodes as blocks of stmts



$= \text{SEQ}(\text{CJUMP}(\llbracket e \rrbracket, t, f), \text{LABEL}(t), \llbracket s \rrbracket, \text{LABEL}(f))$

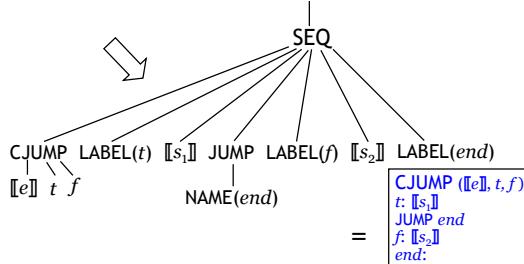
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Translating if-else

$\text{if } (e) s_1 \text{ else } s_2$



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Translating while

$\text{while } (e) s$

loop: CJUMP ([e], t, f)

t: [s]

JUMP(NAME(loop))

f:

$= \text{SEQ}(\text{LABEL}(\text{loop}),$
 $\text{CJUMP}(\llbracket e \rrbracket, t, f),$
 $\text{LABEL}(t),$
 $\llbracket s \rrbracket,$
 $\text{JUMP}(\text{NAME}(\text{loop}))$
 $\text{LABEL}(f))$

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Spec → Implementation

```
abstract class Node { abstract IRnode translate(); ...}
```

$\llbracket \text{if } (e) s \rrbracket = \text{SEQ}(\text{CJUMP}(\llbracket e \rrbracket, t, f), \text{LABEL}(t), \llbracket s \rrbracket, \text{LABEL}(f))$

```
class IfNode { ...
  IRnode translate() {
    SeqNode ret = new SEQ();
    ret.append(new CJUMP(e.translate(), "t", "f"));
    ret.append(new LABEL("t"));
    ret.append(s.translate());
    ret.append(new LABEL("f"));
    return ret;
  }
}
```

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Syntax-directed translation

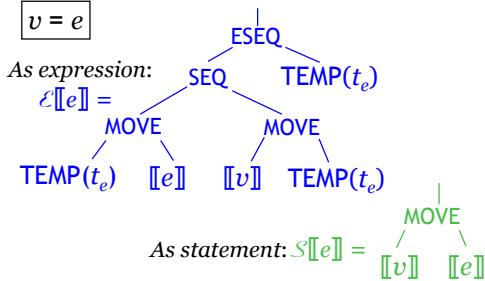
- Translation of any expression or statement expressed in terms of translations of subexpressions
- Can write down translations formally
 - precise specification of what compiler does
 - converts directly to an implementation
 - allows proof that compiler works correctly

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Problem: multiple translations



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Translation functions

- $\mathcal{E}[[e]]$ is IR expr node that computes the same value as expression e (Appel: Ex)
- $\mathcal{E}[[s]]$ is IR expr node that computes the same value as statement s (Appel: Ex)
- $S[[s]]$ is IR stmt node with side-effects of s (Appel: Nx)
- For boolean expr e , $\mathcal{C}[[e, l_1, l_2]]$ is IR statement node that jumps to label l_1 if e evaluates to true and to l_2 if e evaluates to false (Appel: Cx)

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Implementing translations

```
abstract class Node { ... }
abstract IRnode translateE();
abstract IRnode translateS();
abstract IRnode translateC(); ... }

class Assignment {
    Expr variable, value;
    IRnode translateS() {
        return new MOVE(translateE(variable),
                       translateE(value));
    }
    IRnode translateE() { TEMP t = freshTemp();
        return new ESEQ(new SEQ(new MOVE(t,
                                       value.translateE()), new MOVE(...)), t); }
}
```

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Some examples so far

```
 $\mathcal{E}[[v]] = \text{TEMP}(v)$  (for local variable only!)
 $\mathcal{E}[[e_1+e_2]] = \text{ADD}(\mathcal{E}[[e_1]] + \mathcal{E}[[e_2]])$ 
 $S[[v = e]] = \text{MOVE}(\mathcal{E}[[v]], \mathcal{E}[[e]])$ 
 $\mathcal{E}[[v = e]] = \text{ESEQ}(\text{SEQ}(\text{MOVE}(\text{TEMP}(t), \mathcal{E}[[e]]),$ 
 $\text{MOVE}(\mathcal{E}[[v]], \text{TEMP}(t))),$ 
 $\text{TEMP}(t))$ 
 $S[[\text{if } (e) s]] = \text{SEQ}(\text{CJUMP}(\mathcal{E}[[e]], t, f),$ 
 $\text{LABEL}(t), S[[s]], \text{LABEL}(f))$ 
 $\mathcal{E}[[\text{if } (e) s]] = \text{ESEQ}(\text{SEQ}(\dots?...), \text{TEMP}(t))$ 
 $S[[s_1; \dots; s_n]] = ?$ 
```

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Translating a function

- Function body is expression e
- Translate as statement $\mathcal{E}[[e_1+e_2]]$?
- How to translate return statement?
- Idea: introduce return value register $\text{TEMP}(\text{RV})$, *function epilogue* label
- Function body e translated as
 $\text{SEQ}(\text{MOVE}(\text{TEMP}(\text{RV}), \mathcal{E}[[e]]), \text{LABEL}(\text{epilogue}))$
- return e translated as $S[[\text{return } e]] = \text{SEQ}(\text{MOVE}(\text{TEMP}(\text{RV}), \mathcal{E}[[e]]), \text{JUMP}(\text{epilogue}))$

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The boolean operator problem

- How to translate expression $e_1 \& e_2$?
- How about $\mathcal{E}[[e_1 \& e_2]] = \text{AND}(\mathcal{E}[[e_1]], \mathcal{E}[[e_2]])$?
- How about $\mathcal{E}[[e_1 \& e_2]] = \text{ESEQ}(\text{SEQ}(\text{MOVE}(\text{TEMP}(x), 0),$
 $\text{CJUMP}(\mathcal{E}[[e_1]], t_1, \text{no_set}),$
 $\text{LABEL}(t_1), \text{CJUMP}(\mathcal{E}[[e_2]], t_2, \text{no_set}),$
 $\text{LABEL}(t_2), \text{MOVE}(\text{TEMP}(x), 1),$
 $\text{LABEL}(\text{no_set})),$
 $\text{TEMP}(x))$?

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Current translation

- Bad IR: $S[\text{if } (e_1 \& e_2) s] =$
 $\text{SEQ}(\text{CJUMP}(\text{ESEQ}(\text{SEQ}(\text{MOVE}(\text{TEMP}(x), 0),$
 $\text{CJUMP}(\mathcal{C}[e_1], t_1, f),$
 $\text{LABEL}(t_1), \text{CJUMP}(\mathcal{C}[e_2], t_2, f)$
 $\text{LABEL}(t_2), \text{MOVE}(\text{TEMP}(x), 1),$
 $\text{LABEL}(f)),$
 $\text{TEMP}(x)), t, f),$
 $\text{LABEL}(t),$
 $S[s],$
 $\text{LABEL}(f))$
- Better IR: $\text{SEQ}(\text{CJUMP}(\mathcal{C}[e_1], t_1, f), \text{LABEL}(t_1),$
 $\text{CJUMP}(\mathcal{C}[e_2], t_2, f), \text{LABEL}(t_2), S[s], \text{LABEL}(f))$

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Booleans via control

- Idea: representing boolean values via control flow rather than explicitly
- For boolean expr e , $\mathcal{C}[e, l_1, l_2]$ is IR **statement** node that jumps to label l_1 if e evaluates to true and to l_2 if e evaluates to false

$$\mathcal{C}[\text{true}, l_1, l_2] = \text{JUMP}(\text{NAME}(l_1))$$

$$\mathcal{C}[\text{false}, l_1, l_2] = \text{JUMP}(\text{NAME}(l_2))$$

$$\mathcal{C}[e_1 == e_2, l_1, l_2] = \text{CJUMP}(\text{EQ}(\mathcal{C}[e_1], \mathcal{C}[e_2]), l_1, l_2)$$

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Efficient translations of if and &

" $\mathcal{C}[e, l_1, l_2]$ is IR **statement** node that jumps to label l_1 if e evaluates to true and to l_2 if e evaluates to false"

Can use to improve **if** translation:

$$S[\text{if } (e) s] = \text{SEQ}(\mathcal{C}[e, t, f], \text{LABEL}(t), S[s], \text{LABEL}(f))$$

$$\mathcal{C}[e_1 \& e_2, l_1, l_2] = \text{SEQ}(\mathcal{C}[e_1, t, l_2], \text{LABEL}(t), \mathcal{C}[e_2, l_1, l_2])$$

Now: $S[\text{if } (e_1 \& e_2) s] =$
 $\text{SEQ}(\mathcal{C}[e_1, t_1, f], \text{LABEL}(t_1), \mathcal{C}[e_2, t_2, f],$
 $\text{LABEL}(t_2), S[s], \text{LABEL}(f))$: efficient

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Progress

- Now have rules for transforming AST into intermediate representation
- Can apply this to AST of each function defn to get IR for function
- Intermediate representation has many features not found in real assembly code
 - arbitrarily deep expression trees vs. <5 deep
 - ability to perform statements with side-effects as part of an expression (ESEQ, CALL); undefined behavior
 - CJUMP is two-way jump rather than fall-through
- Next time: flattening IR (canonical form)

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