

CS 412/413

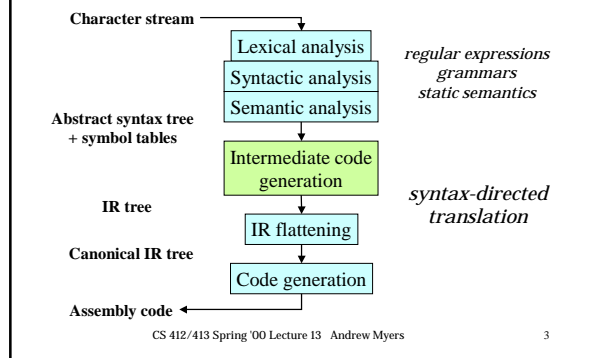
Introduction to Compilers and Translators Cornell University Andrew Myers

Lecture 13: Syntax-directed translation
21 Feb 00

Administration

- Read: Appel 7, 8
- In-class prelim March 1

Where we are



IR expressions

- $CONST(i)$: the integer constant i
- $TEMP(t)$: a temporary register t . The abstract machine has an infinite number of these
- $OP(e_1, e_2)$: one of the following operations
 - arithmetic: ADD, SUB, MUL, DIV, MOD
 - bit logic: AND, OR, XOR, LSHIFT, RSHIFT, ARSHIFT
 - comparisons: EQ, NEQ, LT, GT, LEQ, GEQ
- $MEM(e)$: contents of memory locn w/ address e
- $CALL(f, a_0, a_1, \dots)$: result of fcn f applied to arguments a_i
- $NAME(n)$: address of the statement or global data location labeled n (TBD)
- $ESEQ(s, e)$: result of e after stmt s is executed

IR statements

- $MOVE(dest, e)$: move result of e into $dest$
 - $dest = TEMP(t)$: assign to temporary t
 - $dest = MEM(e)$: assign to memory locn e
- $EXP(e)$: evaluate e , discard result
- $SEQ(s_1, \dots, s_n)$: execute each stmt s_i in order
- $JUMP(e)$: jump to address e
- $CJUMP(e, I_1, I_2)$: jump to statement named I_1 or I_2 depending on whether e is true or false
- $LABEL(n)$: a labeled statement (may be used in NAME, CJUMP)

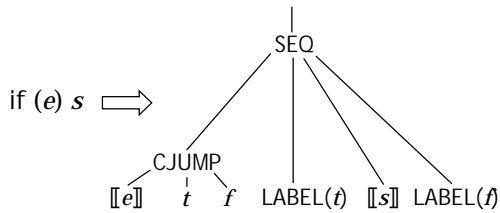
Translation

- Intermediate code generation is tree translation

Abstract syntax tree \Rightarrow IR tree

- Each subtree of AST translated to subtree in IR tree
- Translated version of AST subtree e is IR subtree $[[e]]$

Translating if



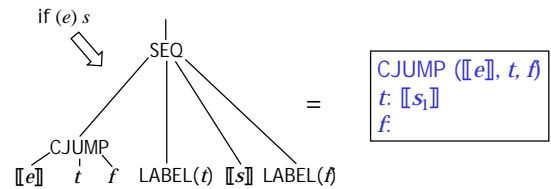
$\llbracket \text{if } (e) \text{ s} \rrbracket = \text{SEQ}(\text{CJUMP}(\llbracket e \rrbracket, t, f), \text{LABEL}(t), \llbracket s \rrbracket, \text{LABEL}(f))$

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How to read IR trees

- Think of SEQ nodes as blocks of stmts

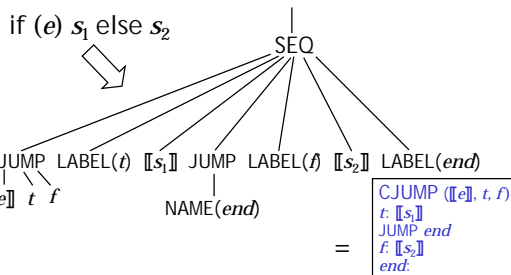


$= \text{SEQ}(\text{CJUMP}(\llbracket e \rrbracket, t, f), \text{LABEL}(t), \llbracket s \rrbracket, \text{LABEL}(f))$

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Translating if-else



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Translating while

while (e) s

loop: CJUMP ($\llbracket e \rrbracket$, t, f)
t: $\llbracket s \rrbracket$
JUMP(NAME(loop))
f:

$= \text{SEQ}(\text{LABEL}(loop),$
CJUMP($\llbracket e \rrbracket$, t, f),
LABEL(t),
 $\llbracket s \rrbracket$,
JUMP(NAME(loop))
LABEL(f))

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Spec \rightarrow Implementation

abstract class Node { abstract IRnode translate(); ... }

$\llbracket \text{if } (e) \text{ s} \rrbracket = \text{SEQ}(\text{CJUMP}(\llbracket e \rrbracket, t, f), \text{LABEL}(t), \llbracket s \rrbracket, \text{LABEL}(f))$

```
class IfNode { ...
  IRnode translate() {
    SeqNode ret = new SEQ();
    ret.append(new CJUMP(e.translate(), "t", "f"));
    ret.append(new LABEL("t"));
    ret.append(s.translate());
    ret.append(new LABEL("f"));
    return ret;
  } ... }
```

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Syntax-directed translation

- Translation of any expression or statement expressed in terms of translations of subexpressions
- Can write down translations formally
 - precise specification of what compiler does
 - converts directly to an implementation
 - allows proof that compiler works correctly

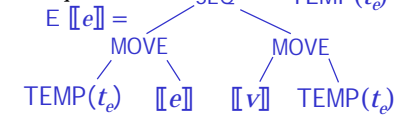
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Problem: multiple translations

$v = e$

As expression:



As statement: $S \llbracket e \rrbracket =$

```

    graph TD
      S[S] --- MOVE3[MOVE]
      MOVE3 --- V[V]
      MOVE3 --- E[E]
  
```

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Translation functions

- $E \llbracket e \rrbracket$ is IR expr node that computes the same value as expression e (Appel: Ex)
- $E \llbracket s \rrbracket$ is IR expr node that computes the same value as statement s (Appel: Nx)
- $S \llbracket s \rrbracket$ is IR stmt node that has same side-effects as statement s (but no value)
- For boolean expr e , $C \llbracket e, I_1, I_2 \rrbracket$ is IR **statement** node that jumps to label I_1 if e evaluates to true and to I_2 if e evaluates to false (Cx)

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Implementing translations

```

abstract class Node { ...
  abstract IRnode translateE();
  abstract IRnode translateS();
  abstract IRnode translateC(); ... }
class Assignment {
  Expr variable, value;
  IRnode translateS() {
    return new MOVE(translateE(variable),
                    translateE(value)); }
  IRnode translateE() { TEMP t = freshTemp();
    return new ESEQ(new SEQ(new MOVE(t,
    value.translateE()), new MOVE(...), t); }
  
```

- Why is this code guaranteed to terminate?

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Some examples so far

```

E [[v]] = TEMP(v)           (variable)
E [[e1+e2]] = ADD(E [[e1]], E [[e2]])
S [[v = e]] = MOVE(E [[v]], E [[e]])
E [[v = e]] = ESEQ(SEQ( MOVE(TEMP(t), E [[e]]),
                       MOVE(E [[v]], TEMP(t))),
                  TEMP(t))
S [[if (e) s]] = SEQ(CJUMP(E [[e]], t, f),
                    LABEL(t), S [[s]], LABEL(f))
E [[if (e) s]] = ESEQ(SEQ(...?), TEMP(t))
E [[s1;...;sn]] = ?
  
```

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Translating a function

- Function body is expression e
- Translate as statement $E \llbracket e_1 + e_2 \rrbracket$?
- How to translate return statement?
- Idea: introduce return value register $TEMP(RV)$, *function epilogue* label
- Function body e translated as $SEQ(MOVE(TEMP(RV), E \llbracket e \rrbracket), LABEL(epilogue))$
- return e translated as $S \llbracket \text{return } e \rrbracket = SEQ(MOVE(TEMP(RV), E \llbracket e \rrbracket), JUMP(epilogue))$

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The boolean operator problem

- How to translate expression e_1 & e_2 ?
- How about $E \llbracket e_1 \& e_2 \rrbracket = \text{AND}(e_1, e_2)$?
- Problem: e_2 always evaluated
- How about $E \llbracket e_1 \& e_2 \rrbracket =$
 $ESEQ(SEQ(MOVE(TEMP(x), 0),$
 $CJUMP(\llbracket e_1 \rrbracket, t1, no_set),$
 $LABEL(t1), CJUMP(\llbracket e_2 \rrbracket, t2, no_set)$
 $LABEL(t2), MOVE(TEMP(x), 1),$
 $LABEL(no_set)),$
 $TEMP(x))$

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Current translation

- Bad IR: $S \llbracket \text{if } (e_1 \ \& \ e_2) \ s \rrbracket =$

```

SEQ(CJUMP(ESEQ(SEQ(MOVE(TEMP(x), 0),
                  CJUMP( $\llbracket e_1 \rrbracket$ , t1, f),
                  LABEL(t1), CJUMP( $\llbracket e_2 \rrbracket$ , t2, f),
                  LABEL(t2), MOVE(TEMP(x), 1),
                  LABEL(f)),
                  TEMP(x)), t, f),
      LABEL(t),
      S  $\llbracket s \rrbracket$ ,
      LABEL(f))

```
- Better IR: $SEQ(CJUMP($\llbracket e_1 \rrbracket$, t1, f), LABEL(t1),$
 $CJUMP($\llbracket e_2 \rrbracket$, t2, f), LABEL(t2), S $\llbracket s \rrbracket$, LABEL(f))$

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Booleans via control

- Idea: representing boolean values via control flow rather than explicitly
 - For boolean expr e , $C \llbracket e, I_1, I_2 \rrbracket$ is IR **statement** node that jumps to label I_1 if e evaluates to true and to I_2 if e evaluates to false
- $C \llbracket \text{true}, I_1, I_2 \rrbracket = \text{JUMP}(\text{NAME}(I_1))$
 $C \llbracket \text{false}, I_1, I_2 \rrbracket = \text{JUMP}(\text{NAME}(I_2))$
 $C \llbracket e_1 == e_2, I_1, I_2 \rrbracket = \text{CJUMP}(\text{EQ}(E \llbracket e_1 \rrbracket, E \llbracket e_2 \rrbracket), I_1, I_2)$

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Efficient translations of if and &

" $C \llbracket e, I_1, I_2 \rrbracket$ is IR **statement** node that jumps to label I_1 if e evaluates to true and to I_2 if e evaluates to false"

- $S \llbracket \text{if } (e) \ s \rrbracket = \text{SEQ}(C \llbracket e, t, f \rrbracket,$
 $\quad \text{LABEL}(t), S \llbracket s \rrbracket,$
 $\quad \text{LABEL}(f))$
- $C \llbracket e_1 \ \& \ e_2, I_1, I_2 \rrbracket = \text{SEQ}(C \llbracket e_1, t, I_2 \rrbracket,$ OR?
 $\quad \text{LABEL}(t),$
 $\quad C \llbracket e_2, I_1, I_2 \rrbracket)$
- Now $S \llbracket \text{if } (e_1 \ \& \ e_2) \ s \rrbracket = \text{SEQ}(C \llbracket e_1, t1, f \rrbracket, LABEL(t1),$
 $C \llbracket e_2, t2, f \rrbracket, LABEL(t2), S \llbracket s \rrbracket, LABEL(f))$: efficient

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Progress

- Now have rules for transforming AST into intermediate representation
- Can apply this to AST of each function defn to get IR for function
- Intermediate representation has many features not found in real assembly code
 - arbitrarily deep expression trees vs. 1-2 deep
 - ability to perform statements with side-effects as part of an expression (ESEQ, CALL); undefined behavior
 - CJUMP is two-way jump rather than fall-through
- Why do we allow this in IR at all?

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